# Jason A. Hanna

## Education

**B.S./M.S. ELECTRICAL ENGINEERING | 2021 - 2025 (I.P.) | ROCHESTER INSTITUTE OF TECHNOLOGY**

- GPA: 4.0
- Bachelor's Degree expected Spring 2025, Master's Degree expected Spring 2026

## Experience

**ELECTRICAL ENGINEERING CO-OP | MICROSTRAIN BY HBK | MAY 2024 – AUGUST 2024**

- Led Design of Industrial-Grade RF Communication PCB incorporating Buck-Boost Converter, LDO, ESD protection, USB and RS-232 peripherals, Hardware signal filtration, and Transmission Lines
- Met Specifications Required for Government Contract within accelerated timeframe. Finalized release set to be included in main product line

**ELECTRICAL ENGINEERING CO-OP | MICROSTRAIN BY HBK | MAY 2023 – JANUARY 2023**

- Integrated Microstrain GQ7 Device within Raspberry Pi GPSD via gpsmon interface
- Redesigned, Manufactured, and Debugged Microstrain C-Series Connectivity Board
- Designed CV8/INS-8 Platform Board Utilizing ADIS16607 MEMS IMU & LPS22DF Pressure Sensors
- Evaluated performance of STM and Bosch Pressure Sensors in varying environmental conditions

**ELECTRICAL ENGINEERING INTERNSHIP | PARKER-LORD MICROSTRAIN | MAY 2022 – AUGUST 2022**

- Performed research project on Ethernet Circuits and Stratum Servers
- Successfully achieved PPS Synchronization between multiple clients using DB9 RS-232 connectors along with creation of PPS host
- Designed GNSS Tester Board for Quectel LG69TAAMD device with Altium Designer

**ELECTRONIC ASSEMBLER | INTEGRITY DESIGN | 2016 - 2022**

- Assembled, constructed, and calibrated circuit boards for Electromagnetic Gaussmeter Models:
    - IDR 309, IDR 321, IDR 329

**IBM APPRENTICESHIP | MAR 2021 – JUNE 2021**

- Developed Python Application which scraped individual stock prices using Beautiful Soup and generated corresponding graphs with Pandas

**STUDENT RESEARCH ASSISTANT | UNIVERSITY OF VERMONT | JUNE 2020 – MAY 2021**

- Researched Tetrahedral Cage Molecules using Chemical Modeling Software and directories in collaboration with Professor Dr. Schneebeli and Ph.D. students

## Skills/Proficiencies

- Cadence Virtuoso
- Altium Designer
- LTspice
- Quartus
- Verilog, VHDL
- C/C++, Python, MATLAB, ASM, Java, Linux
- Engineering Content Creation: [Channel Link](#) ▶

## Awards

- 6x RIT Dean's List with Distinction
- RIT Presidential Scholars Award
- RIT Recognition Scholar
- Vermont State Scholar